

Amendments to the Specification:

Please amend the specification as follows:

Please replace paragraph starting at page 12, line 7, with the following rewritten paragraph:

Core 2 has first to fourth input terminals and first to third output terminals. Similar to the case of the core 1, the first to third input terminals and the first to third output terminals are connected to corresponding terminals "ai" and "ao" "ci" and "co" of the test circuits 13 to 15. The fourth input terminal is connected to terminal "2ci" of circuit 20 which is explained later.

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